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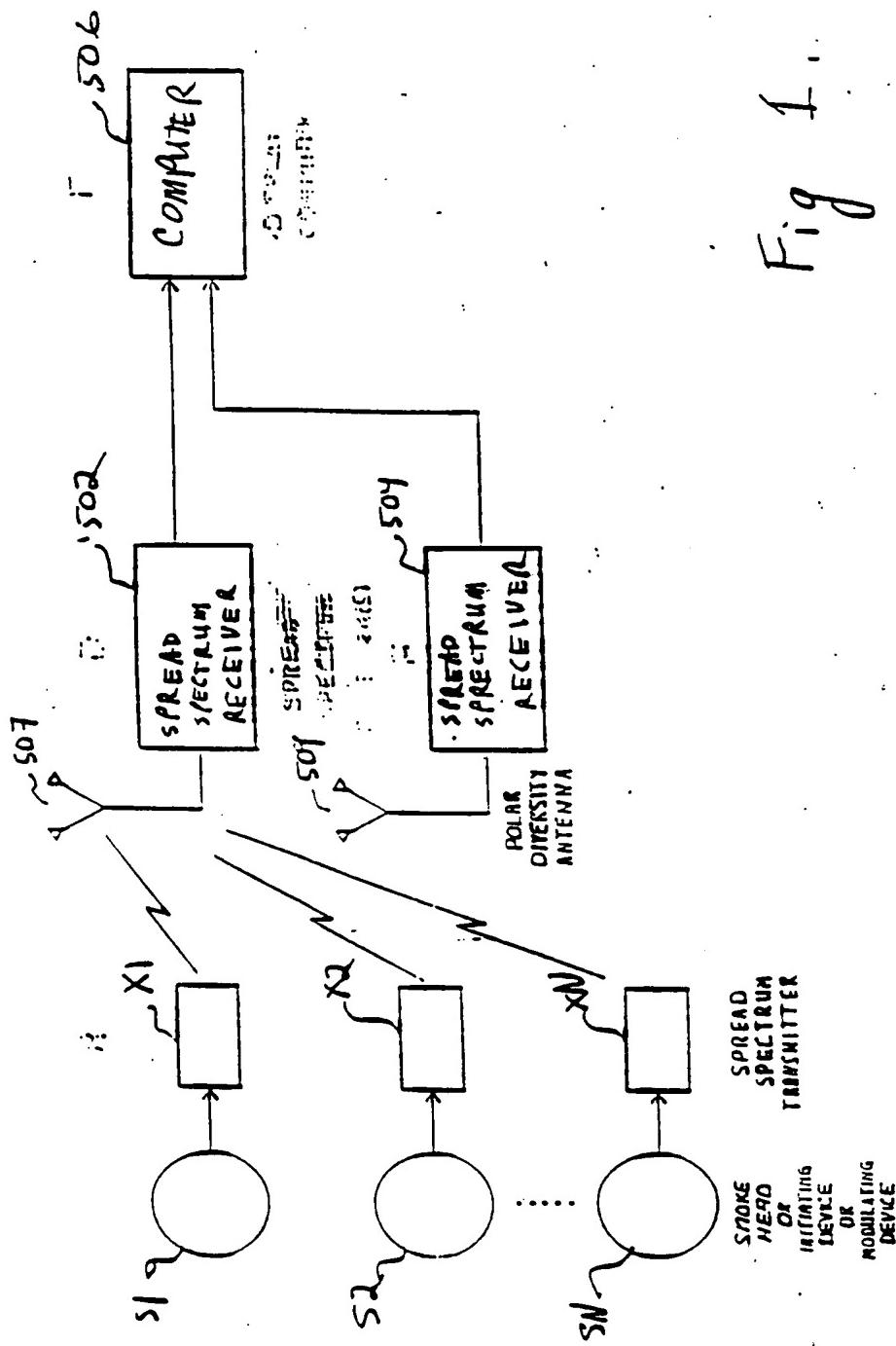


Fig 1.

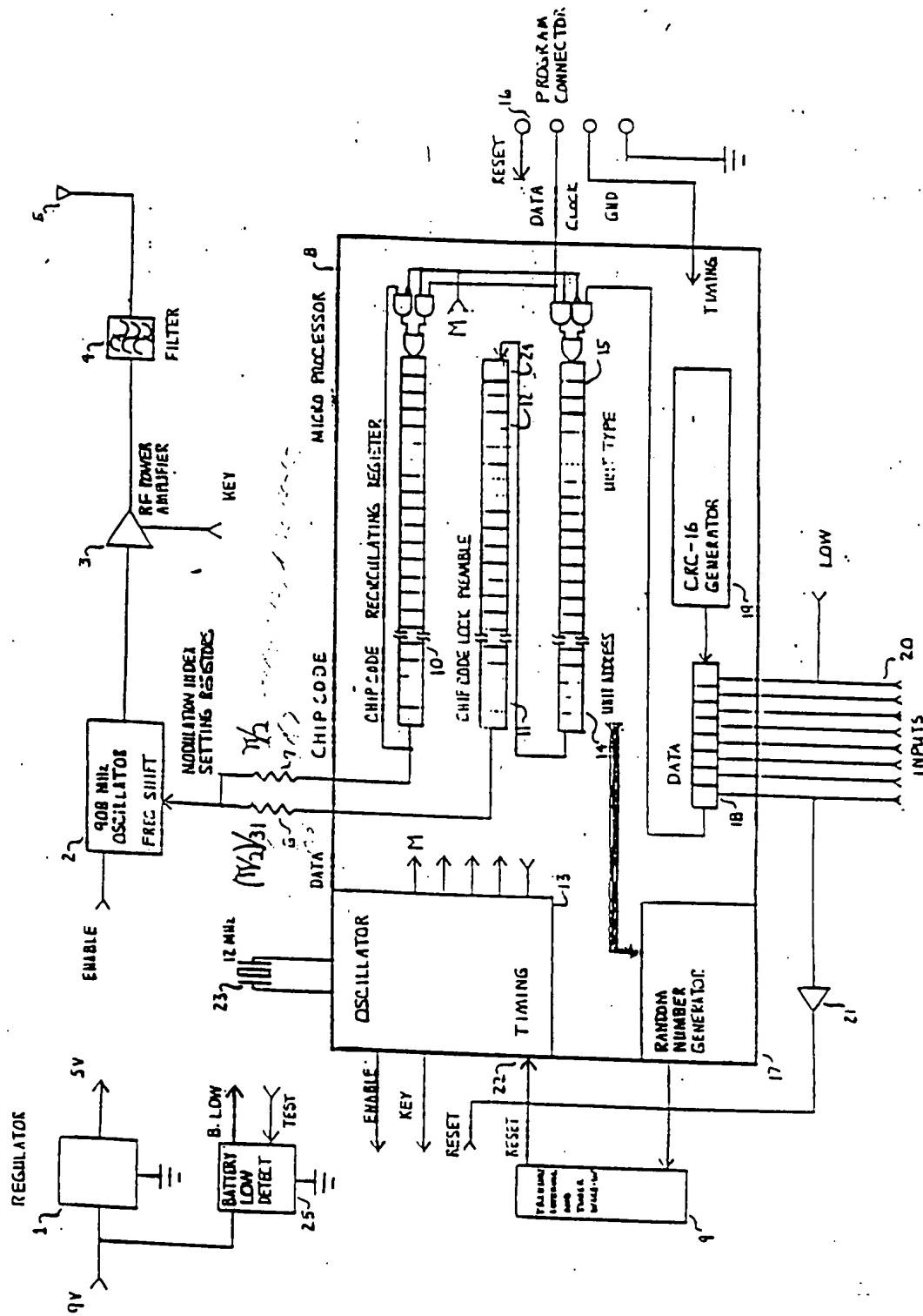


Fig.

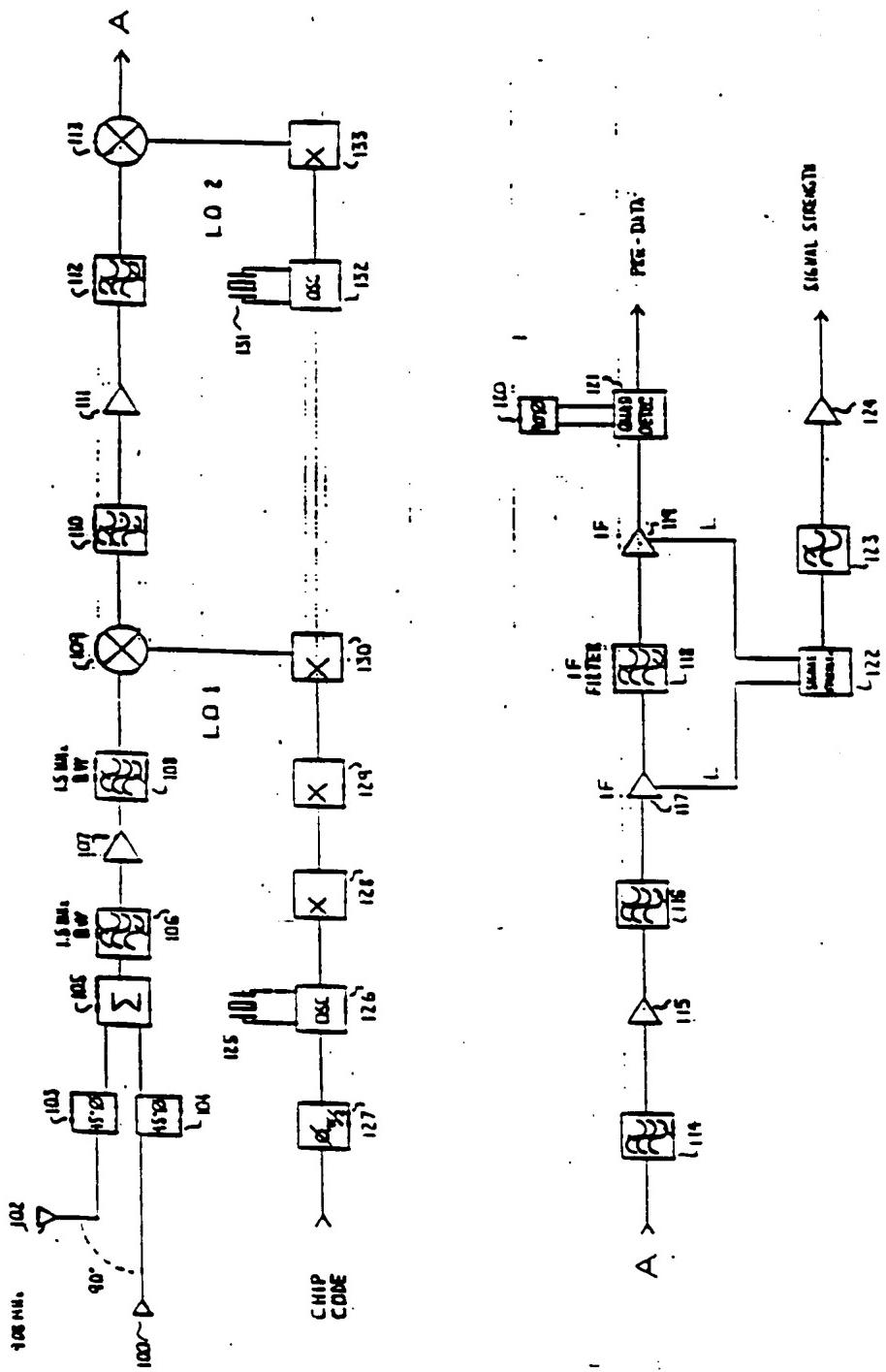


Fig. 3A

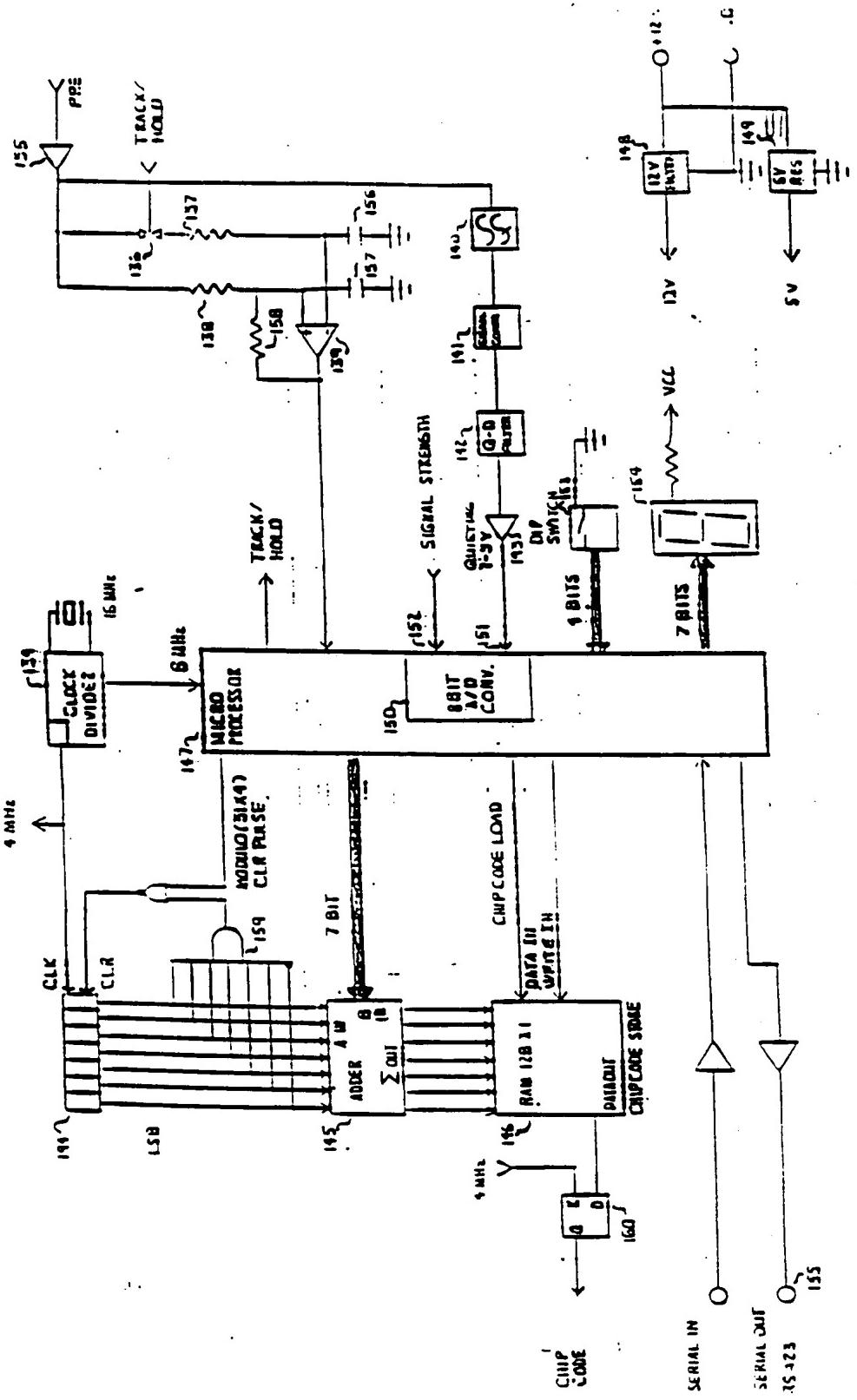


Fig 3B

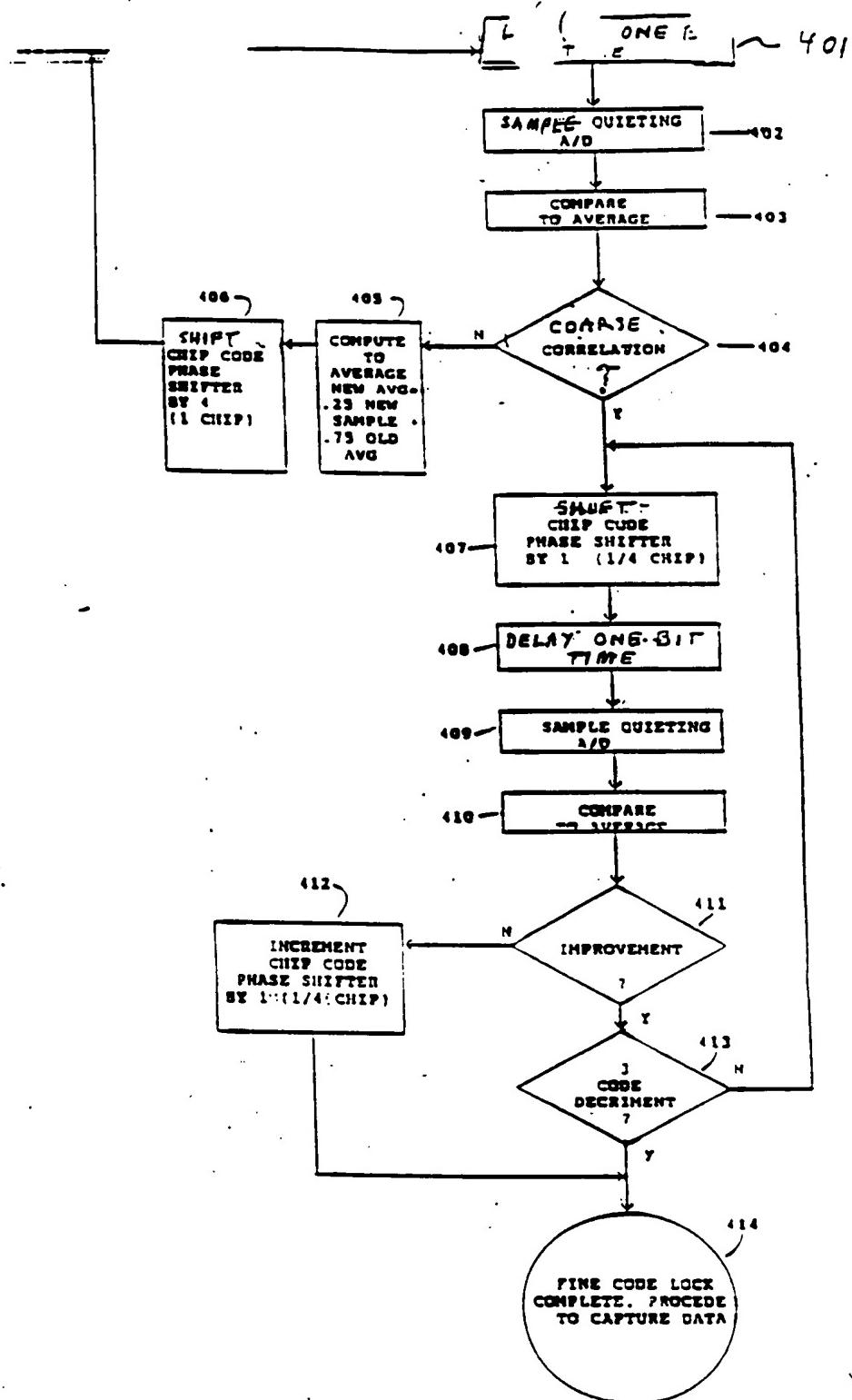


Fig. 4

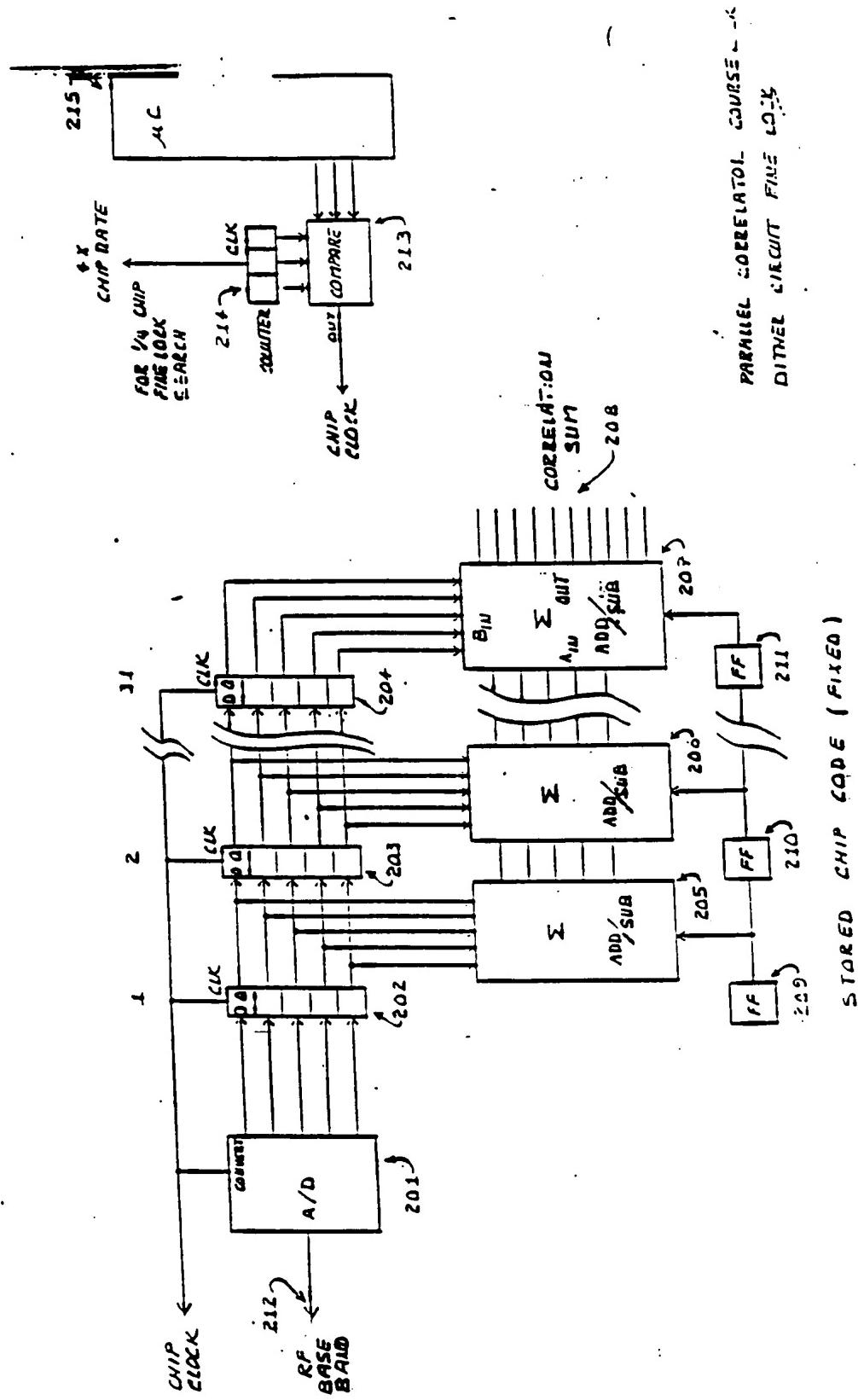


Fig 6

STORED CHIP CODE (frozen)

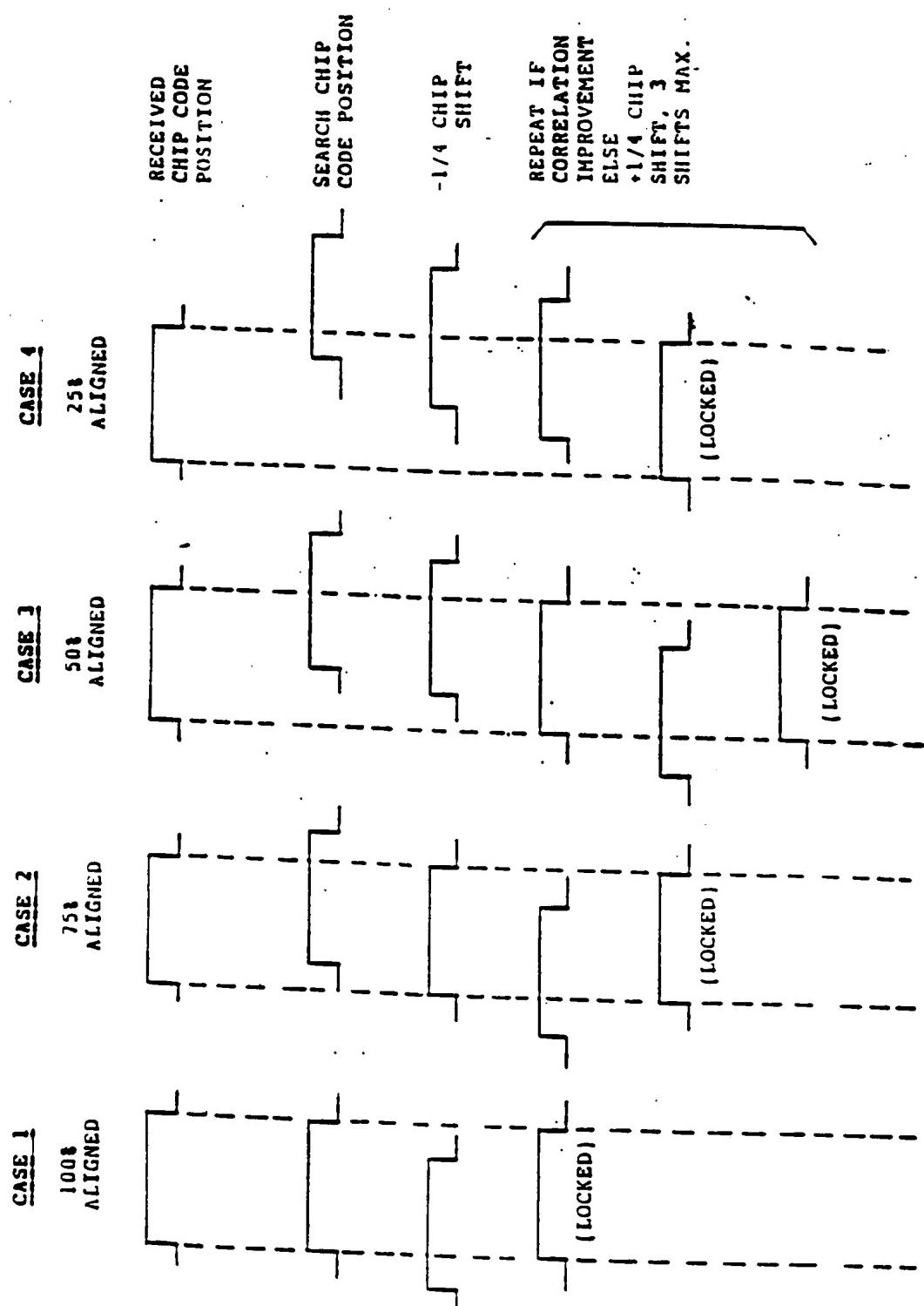


Fig 5

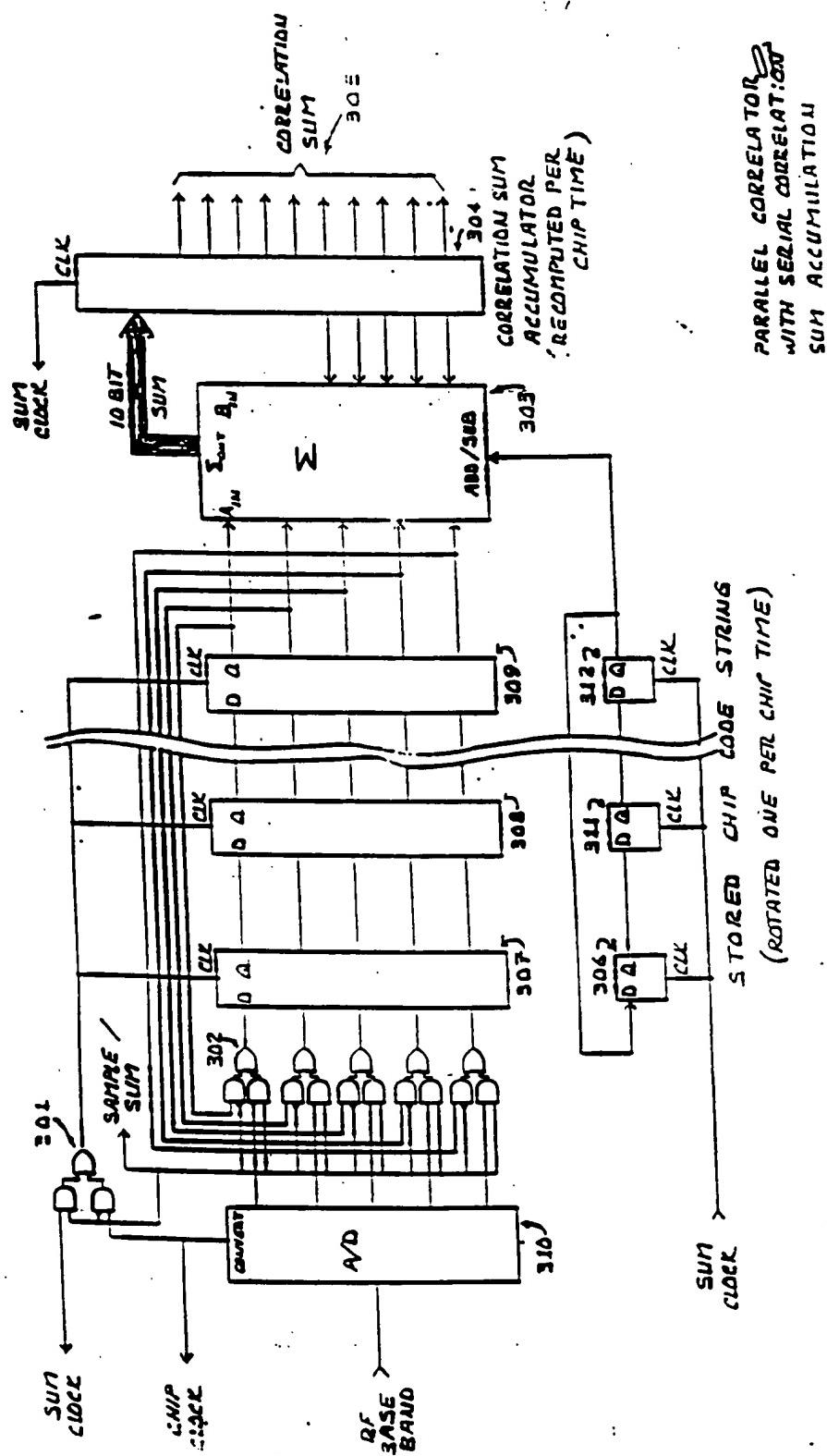


Fig 7.